

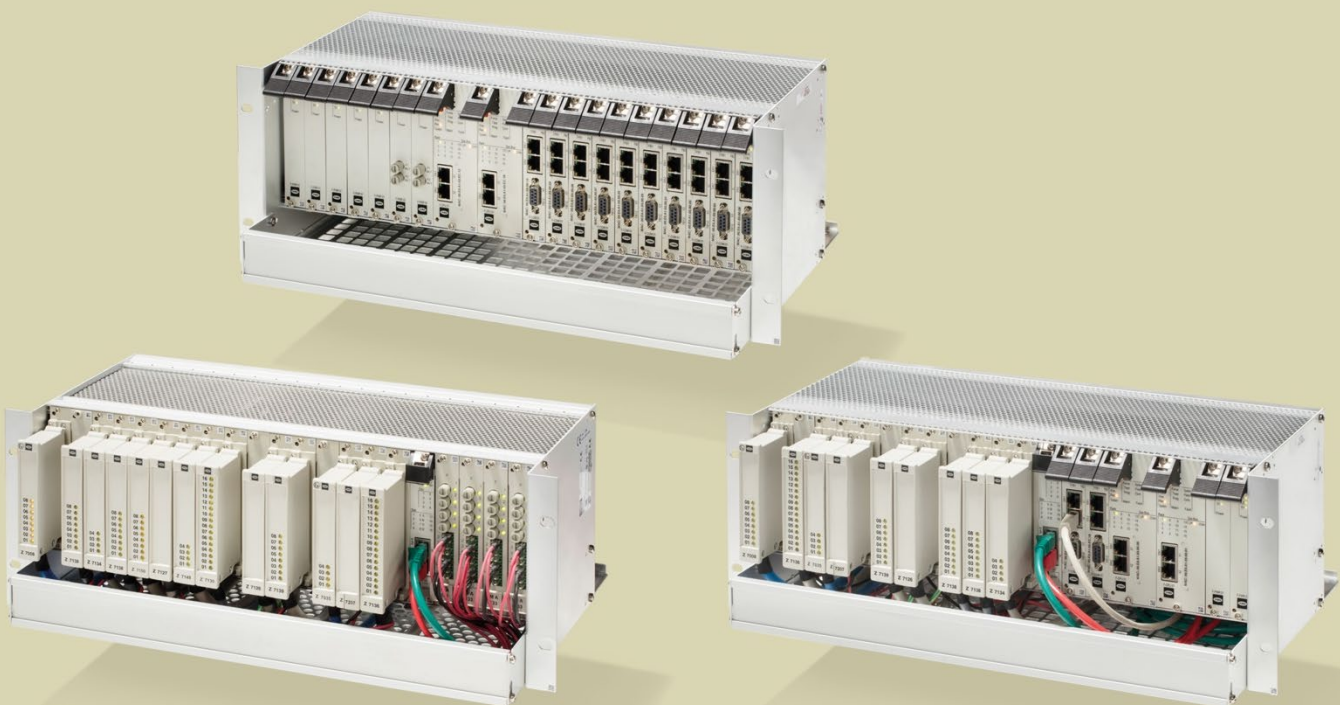
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# HIQuad<sup>®</sup>X

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Release Notes 2023/01

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# 1 HIQuad X Operating System Versions

This manual describes the improvements, new functions, and restrictions of new HIQuad X operating system versions compared to the previous versions.

The following table lists the most current operating systems (OS) and associated loaders (OS loader, OSL) used in the HIQuad X system:

Module	OS version	Operating system	Designation
F-CPU 01	<b>V14.6 (New)</b>	Processor module	HQX_CPU5x_HA1_OS_V14.6.ldb
	V10.0	Loader for the processor module	HQX_CPU5x_HA1_OSL_V10.0.ldb
F-IOP 01	V10.24	I/O processing module	HQX_IOP0x_HA1_OS_V10.24.ldb
	V10.0	Loader for the I/O processing module	HQX_IOP0x_HA1_OSL_V10.0.ldb
F-COM 01	V14.12	Communication module	HQXCOM_HA1_OS_V14.12.ldb
	V10.0	Loader for the communication module	HQXCOM_HA1_OSL_V10.0.ldb

Table 1: HIQuad X Operating Systems and Associated Loader

Additionally, take the release notes for SILworX V14.0.0 into account.

## 1.1 Checksums of Operating System Versions

The following table specifies the checksums of the latest operating system versions:

Module	OS version	Safety CRC	SHA256
F-CPU 01	<b>V14.6 (New)</b>	0x640F_3B75	2F15.A03C.6DF2.8D57.DF7A.B2CE.2BA5.AF12. F57B.7ABF.EAF2.4746.639D.8681.3C9F.5786
F-IOP 01	V10.24	0x93E6_8D71	8D83.9057.B8B1.DE8C.2D7D.896B.8E31.EBFA. F253.B6F4.87A4.F0A9.E12C.3ED0.265D.7564
F-COM 01	V14.12	0xD9B2_8372	906C.85A7.C7CD.0130.AA83.6425.592E.63A9. F532.D105.5A8C.CBC6.071A.03EC.0B72.79DC

Table 2: Checksums

## 2 New Functions

The new CPU operating system version (CPU OS  $\geq$  V14) ensures increased login protection for HIQuad X controllers. This measure provides compliance with the requirements in accordance with IEC 62443-4-2 / SL1.

It also allows successful and failed logins to be evaluated using system variables in the user program. For further details on the new system variables, refer to the HIQuad X system manual (HI 803 211 E).

### 3 Improvements

None.

## 4 Restrictions

- The new CPU operating system version for the F-CPU 01 module (V13.8 and higher) optimizes the processors' memory access. Depending on the system structure, the application program and the associated workload, the cycle time can increase by up to 5%.
- The use of the HIQuad X system in conjunction with the F-IOP 01 I/O processing module for systems with a high demand rate ("high-demand" applications) is not permitted without additional workarounds. Affected customers are informed of this in a separate letter.



## 5 Updates to New Operating System Versions

HIMA recommends always using the latest operating system version of a module. To update an existing system, system downtimes can for instance be used to update the operating systems and operating system loaders (OSL) on all modules.

In the HIQuad X system, processor modules, I/O processing modules and communication modules can load the operating system version during operation. This applies provided a) the overall structure is redundant and uses I/O extension racks b) the modules within the system are in proper working order.

Refer to the maintenance manual for details on how to load operating systems.

Only operating system versions that are released for common operation may be loaded in HIQuad X modules! The compatibilities to the different operating system versions described in the release notes must be observed.

### 5.1 Loading New Operating System Versions and Loaders to Several Modules

When loading several modules, e.g., to update an entire system, HIMA recommends following the sequence as specified in Table 3. It is possible to deviate from this recommendation; however, when doing so, ensure that the redundant modules are promptly updated to the same operating system version. Additionally, ensure that processor modules are updated one after the other.

Observe the *restrictions* in the corresponding release notes.

Step	Modules	Connect the PADT to:
1.	I/O processing modules, UP loop	Communication module or processor module
2.	I/O processing modules, DOWN loop	Communication module or processor module
3.	All communication modules	Processor module
4.	All processor modules	Communication module, processor module
Notice: The simultaneous use of processor modules with differing operating system versions is only allowed for the duration of the update! If processor modules are operated with differing operating system versions, a warning appears. The processor modules must be updated one after the other, without performing any actions in between!		

Table 3: Recommended Order for Loading to Several Modules

## 6 References

- HIQuad X system manual, HI 803 211 E.
- HIQuad X safety manual, HI 803 209 E.
- HIQuad X maintenance manual, HI 803 213 E.
- Communication manual, HI 801 101 E.

### 6.1 Zstandard License Terms

The CPU operating systems contain a Zstandard decompression algorithm provided with the following license terms:

BSD License

For Zstandard software

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## 6.2 Apache License

The COM operating system includes an SSL/TLS library Mbed TLS licensed by ARM Limited <http://www.apache.org/licenses/LICENSE-2.0>.

### 6.2.1 Mbed TLS Adaptations

Index: include/mbedtls/config.h

```
=====
--- include/mbedtls/config.h (revision 28)
+++ include/mbedtls/config.h (revision 29)
@@ -1.3 +1.4 @@
+// Modifications copyright (C) 2018 HIMA Paul Hildebrandt GmbH
/**
 * \file config.h
 *
@@ -33.6 +34.13 @@
#define _CRT_SECURE_NO_DEPRECATED 1
#endif
+// Start of HIMA modification
+//error Wrong File - include platform specific configuration file from /mbedtls
(MBEDTLS_CONFIG_FILE is not set)
+// Falls dieser Fehler auftritt muss im entsprechenden Makefile (aus dem diese Datei gezogen
wird)
+// folgende Anpassung gemacht werden (siehe auch mbedtls.mke)
+// include <pfad_zu>p9tool/mbedtls/mbedtls_make.inc
+// End of HIMA modification
+
/**
 * \name SECTION: System support
 *
```

Index: library/timing.c

```
=====
--- library/timing.c (revision 28)
+++ library/timing.c (revision 29)
@@ -1.3 +1.4 @@
+// Modifications copyright (C) 2018 HIMA Paul Hildebrandt GmbH
/*
 * Portable interface to the CPU cycle counter
 *
@@ -372.7 +373.10 @@
#endif /* !MBEDTLS_TIMING_ALT */
#ifndef MBEDTLS_SELF_TEST
+// Start of HIMA modification
+//if defined(MBEDTLS_SELF_TEST) && defined(MBEDTLS_SSL_PROTO_DTLS)
+// HIMA: Test nur wenn DTLS aktiv - Ohne DTLS wird nur ein Teil von timing.c genutzt
(mbedtls_timing_hardclock)
+// End of HIMA modification
/*
 * Busy-waits for the given number of milliseconds.

```

Index: .

```
=====
--- . (revision 28)
+++ . (revision 29)
Property changes on: .
```

---

Added: svn:mergeinfo

## -0.0 +0.2 ##

Merged /mbedtls/mbedtls-2.5.1:r22-23

Merged /mbedtls/mbedtls-2.14.1:r26-27



## Appendix

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## Release Notes

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### HI 803 284 E

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